

CURRICULUM VITAE



Current Position	Vice Minister of MSIP
Name	Yoon Jong-Lok
Date of Birth	December 17, 1957
Inauguration Date	March 25, 2013

Education

2003 AMP (Advanced Management Program), Seoul National University
1995-1996 Diploma in Telecommunications, Michigan State University, USA
1990-1992 M.S. in Electronics Engineering, Yonsei University
1976-1980 B.S. in Telecommunication Engineering, Korea Aerospace University

Professional Career

Present Vice Minister, Ministry of Science, ICT and Future Planning (MSIP)
2012 Professor, School of Integrated Technology, Yonsei Institute of Convergence Technology
2009.12~2011.9 Bell Labs USA (Senior Executive Consultant)
2009-2011 Special Researcher, Bell Laboratories
2007-2009 Head of the Growth Business Sector (Executive Director, Vice Chairman of KT)
2006 Director General, Basic Research Bureau, Ministry of Science and Technology (MOST)
2005 Seconded, Central Officials Training Institute

2003	Director General for Public Relations, MOST
2000	Director, Overall Coordination Division, MOST
1999	Director, Strategic Technology Development Division, MOST
1996	Director, S&T Manpower Development Division, S&T Promotion Division, Atomic Energy Development Division, MOST
1994	Overseas Training, the University of Manchester, U.K.
1992	President, DAEEOK Valley Management Office, MOST
1986	Research and Development Policy Division (Chemistry Engineering, Power, Basic Science, Research Planning), MOST
1980-1984	DAEEOK Valley Management Office, (MOST)

Rewards

April 1987	Received a Accolade from Minister
June 1997	Awarded a Medal